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The Ministry of Economy, Trade and Industry has conducted the development of Advanced Parallelizing Compiler (APC) Technology with a three-year plan since 2000 in an aim to establish next-generation software (compiler) technology which will enhance the capacity of the practical effect dramatically. In this last year of the plan, we will hold a symposium and present achievements of the program research and development so that many people can recognize and apply them.

The government is now strategically promoting research and development of infrastructure technology, which is required to make Japan "the world's forefront IT nation," based on e-Japan Project and e-Japan Priority Policy Program. In a transition from existing computing technology with a focus on hardware to next-generation computing technology, which is integrated with software, this program has established the next-generation technology through cooperation among industry, academia and government, and developed the most advanced software (compiler) in the world at present.

We hope that the achievements of our research and development will enhance the capacity of the practical effect, convenience, and cost-performance, contribute to better competitiveness of IT industry through their application in development and practical use, widely from PCs to high-performance computers, and contribute to stiff global competitiveness through more efficient researches and developments in various fields such as global environment and gene-analysis, using high-performance computers.

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